

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3	instance with (thread same interconnect\$5) and (design\$5 with (circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/19 11:53
L2	2	instance with (thread same interconnect\$5) and (integrated near circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/19 11:55
L3	0	instance with ( interconnect\$5) and design\$5 with (circuit same thread ) and (integrated near circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/19 11:58
L4	40	instance with ( interconnect\$5) and (circuit same thread ) and (integrated near circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/19 11:58
L5	1	instance with ( interconnect\$5) and (circuit near thread ) and (integrated near circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/19 11:58
L6	1	instance with ( interconnect\$5 same thread) and (circuit same thread ) and (integrated near circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/19 12:12
L7	2	instance with ( interconnect\$5 same thread) and (integrated near circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/19 12:13

L8	16	(instance same interconnect\$5 same thread) and (design\$5 with ( circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2009/06/19 12:14
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6/19/2009 12:31:29 PM

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